Green Computing Platforms Step1. Pipeline, Superscalar and VLIW

IS/BS/MS

No.

Name

**●Describe obstacles for simultaneous execution of instructions**

●**Basic idea to remove dependency**

**●What is final safety mechanism for debugging programs on superscalars?**

**●Why is VLIW so low power?**

**●Software pipelining**

**●Loop unrolling**

**●Trace scheduling**

**●Percolation scheduling**

**●Architectural support for compilers**